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(71) Applicant (for DE only): **PHILIPS INTELLECTUAL
PROPERTY & STANDARDS GMBH** [DE/DE]; Stein-
damm 94, 20099 Hamburg (DE).

(71) Applicant (for all designated States except DE, US):
KONINKLIJKE PHILIPS ELECTRONICS N. V.
[NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven
(NL).

(71) Applicant (for DE only): **LUMILEDS LIGHTING US.
LLC** [US/US]; 370 West Trimble Road, Ms 91 Uk, San,
José, CA 95131-1008, USA, CA 95131-1008 (US).

(72) Inventors: **MUELLER, Gerd O.**; c/o Philips Intellec-
tual Property & Standards GmbH, Weissshausstr. 2, 52066
Aachen (DE). **MUELLER-MACH, Regina B.**; c/o Philips
Intellectual Property & Standards GmbH, Weissshausstr. 2,
52066 Aachen (DE). **KRAMES, Michael R.**; c/o Philips
Intellectual Property & Standards GmbH, Weissshausstr.
2, 52066 Aachen (DE). **SCHMIDT, Peter J.**; c/o Philips
Intellectual Property & Standards GmbH, Weissshausstr.
2, 52066 Aachen (DE). **BECHTEL, Helmut**; c/o Philips
Intellectual Property & Standards GmbH, Weissshausstr.
2, 52066 Aachen (DE). **MEYER, Joerg**; c/o Philips
Intellectual Property & Standards GmbH, Weissshausstr.
2, 52066 Aachen (DE). **DE GRAAF, Jan**; c/o Philips
Intellectual Property & Standards GmbH, Weissshausstr.
2, 52066 Aachen (DE). **KOP, Theo Arnold**; c/o Philips
Intellectual Property & Standards GmbH, Weissshausstr. 2,
52066 Aachen (DE).

(74) Agent: **VOLMER, Georg**; Philips Intellectual Property &
Standards GmbH, Weissshausstr. 2, 52066 Aachen (DE).

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(54) Title: LUMINESCENT CERAMIC FOR A LIGHT EMITTING DEVICE



(57) Abstract: A semiconductor light emitting device comprising a light emitting layer disposed between an n-type region and a p-type region is combined with a ceramic layer which is disposed in a path of light emitted by the light emitting layer. The ceramic layer is composed of or includes a wavelength converting material such as a phosphor. Luminescent ceramic layers according to embodiments of the invention may be more robust and less sensitive to temperature than prior art phosphor layers. In addition, luminescent ceramics may exhibit less scattering and may therefore increase the conversion efficiency over prior art phosphor layers.

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Luminescent ceramic for a light emitting device

5 The present invention relates to wavelength converted semiconductor light emitting devices.

 Light emitting diodes (LEDs) are well-known solid state devices that can generate light having a peak wavelength in a specific region of the light spectrum. LEDs are typically used as illuminators, indicators and displays. Traditionally, the most
10 efficient LEDs emit light having a peak wavelength in the red region of the light spectrum, i.e., red light. However, III-nitride LEDs have been developed that can efficiently emit light having a peak wavelength in the UV to green region of the spectrum. III-nitride LEDs can provide significantly brighter output light than traditional LEDs.

15 In addition, since light from III-nitride devices generally has a shorter wavelength than red light, the light generated by the III-nitride LEDs can be readily converted to produce light having a longer wavelength. It is well known in the art that light having a first peak wavelength (the "primary light") can be converted into light having a longer peak wavelength (the "secondary light") using a process known as
20 luminescence/fluorescence. The fluorescent process involves absorbing the primary light by a wavelength-converting material such as a phosphor, exciting the luminescent centers of the phosphor material, which emit the secondary light. The peak wavelength of the secondary light will depend on the phosphor material. The type of phosphor material can be chosen to yield secondary light having a particular peak wavelength.
25

 With reference to FIG. 1, a prior art phosphor LED 10 described in U.S. Patent 6,351,069 is shown. The LED 10 includes a III-nitride die 12 that generates blue primary light when activated. The III-nitride die 12 is positioned on a reflector cup
30 lead frame 14 and is electrically coupled to leads 16 and 18. The leads 16 and 18 conduct electrical power to the III-nitride die 12. The III-nitride die 12 is covered by a

layer 20, often a transparent resin, that includes wavelength-converting material 22. The type of wavelength-converting material utilized to form the layer 20 can vary, depending upon the desired spectral distribution of the secondary light that will be generated by the fluorescent material 22. The III-nitride die 12 and the fluorescent layer 20 are encapsulated by a lens 24. The lens 24 is typically made of a transparent epoxy or silicone.

In operation, electrical power is supplied to the III-nitride die 12 to activate the die. When activated, die 12 emits the primary light away from the top surface of the die. A portion of the emitted primary light is absorbed by the wavelength-converting material 22 in the layer 20. The wavelength-converting material 22 then emits secondary light, i.e., the converted light having a longer peak wavelength, in response to absorption of the primary light. The remaining unabsorbed portion of the emitted primary light is transmitted through the wavelength-converting layer, along with the secondary light. The lens 24 directs the unabsorbed primary light and the secondary light in a general direction indicated by arrow 26 as output light. Thus, the output light is a composite light that is composed of the primary light emitted from die 12 and the secondary light emitted from the wavelength-converting layer 20. The wavelength-converting material may also be configured such that very little or none of the primary light escapes the device, as in the case of a die that emits UV primary light combined with one or more wavelength-converting materials that emit visible secondary light.

As III-nitride LEDs are operated at higher power and higher temperature, the transparency of the organic encapsulants used in layer 20 tend to degrade, undesirably reducing the light extraction efficiency of the device and potentially undesirably altering the appearance of the light emitted from the device. Several alternative configurations of wavelength-converting materials have been proposed, such as growth of LED devices on single crystal luminescent substrates as described in U.S. Patent 6,630,691, thin film phosphor layers as described in U.S. Patent 6,696,703, and conformal layers deposited by electrophoretic deposition as described in U.S. Patent 6,576,488 or stenciling as described in U.S. Patent 6,650,044. However, one major disadvantage of prior solutions is the optical heterogeneity of the phosphor/encapsulant system, which leads to scattering, potentially causing losses in

conversion efficiency.

In accordance with embodiments of the invention, a semiconductor light emitting device comprising a light emitting layer disposed between an n-type region and a p-type region is combined with a ceramic layer which is disposed in a path of light emitted by the light emitting layer. The ceramic layer is composed of or includes a wavelength converting material such as a phosphor. Luminescent ceramic layers according to embodiments of the invention may be more robust and less sensitive to temperature than prior art phosphor layers. In addition, luminescent ceramics may exhibit less scattering and may therefore increase the conversion efficiency over prior art phosphor layers.

Fig. 1 illustrates a prior art phosphor-converted semiconductor light emitting device.

Fig. 2 illustrates a flip chip semiconductor light emitting device including a ceramic phosphor layer.

Fig. 3 illustrates a semiconductor light emitting device including a bonded host substrate and a ceramic phosphor layer.

Fig. 4 illustrates an example of a doping profile in a ceramic phosphor layer.

Fig. 5 illustrates a semiconductor light emitting device including multiple ceramic layers.

Fig. 6 illustrates a semiconductor light emitting device including a shaped ceramic phosphor layer.

Fig. 7 illustrates a semiconductor light emitting device including a ceramic phosphor layer wider than the epitaxial layers in the device.

Fig. 8 illustrates a semiconductor light emitting device including a ceramic phosphor layer and a heat extraction structure.

The above-mentioned devices with thin film or conformal phosphor layers can be difficult to handle because the phosphor layers tend to be fragile. In

accordance with embodiments of the invention, wavelength converting materials such as phosphors are formed into ceramic slabs, referred to herein as "luminescent ceramics." The ceramic slabs are generally self-supporting layers formed separately from the semiconductor device, then attached to the finished semiconductor device or used as a growth substrate for the semiconductor device. The ceramic layers may be translucent or transparent, which may reduce the scattering loss associated with non-transparent wavelength converting layers such as conformal layers. Luminescent ceramic layers may be more robust than thin film or conformal phosphor layers. In addition, since luminescent ceramic layers are solid, it may be easier to make optical contact to additional optical elements such as lenses and secondary optics, which are also solid.

Examples of phosphors that may be formed into luminescent ceramic layers include aluminum garnet phosphors with the general formula $(\text{Lu}_{1-x-y-a-b}\text{Y}_x\text{Gd}_y)_3(\text{Al}_{1-z}\text{Ga}_z)_5\text{O}_{12}:\text{Ce}_a\text{Pr}_b$ wherein $0 < x < 1$, $0 < y < 1$, $0 < z \leq 0.1$, $0 < a \leq 0.2$ and $0 < b \leq 0.1$, such as $\text{Lu}_3\text{Al}_5\text{O}_{12}:\text{Ce}^{3+}$ and $\text{Y}_3\text{Al}_5\text{O}_{12}:\text{Ce}^{3+}$ which emit light in the yellow-green range; and $(\text{Sr}_{1-x-y}\text{Ba}_x\text{Ca}_y)_{2-z}\text{Si}_{5-a}\text{Al}_a\text{N}_{8-a}\text{O}_a:\text{Eu}_z^{2+}$ wherein $0 \leq a < 5$, $0 < x \leq 1$, $0 \leq y \leq 1$, and $0 < z \leq 1$ such as $\text{Sr}_2\text{Si}_5\text{N}_8:\text{Eu}^{2+}$, which emit light in the red range. Suitable $\text{Y}_3\text{Al}_5\text{O}_{12}:\text{Ce}^{3+}$ ceramic slabs may be purchased from Baikowski International Corporation of Charlotte, NC. Other green, yellow, and red emitting phosphors may also be suitable, including $(\text{Sr}_{1-a-b}\text{Ca}_b\text{Ba}_c)\text{Si}_x\text{N}_y\text{O}_z:\text{Eu}_a^{2+}$ ($a = 0.002 - 0.2$, $b = 0.0 - 0.25$, $c = 0.0 - 0.25$, $x = 1.5 - 2.5$, $y = 1.5 - 2.5$, $z = 1.5 - 2.5$) including, for example, $\text{SrSi}_2\text{N}_2\text{O}_2:\text{Eu}^{2+}$; $(\text{Sr}_{1-u-v-x}\text{Mg}_u\text{Ca}_v\text{Ba}_x)(\text{Ga}_{2-y-z}\text{Al}_y\text{In}_z\text{S}_4):\text{Eu}^{2+}$ including, for example, $\text{SrGa}_2\text{S}_4:\text{Eu}^{2+}$; $\text{Sr}_{1-x}\text{Ba}_x\text{SiO}_4:\text{Eu}^{2+}$; and $(\text{Ca}_{1-x}\text{Sr}_x)\text{S}:\text{Eu}^{2+}$ wherein $0 < x \leq 1$ including, for example, $\text{CaS}:\text{Eu}^{2+}$ and $\text{SrS}:\text{Eu}^{2+}$.

A luminescent ceramic may be formed by heating a powder phosphor at high pressure until the surface of the phosphor particles begin to soften and melt. The partially melted particles stick together to form a rigid agglomerate of particles. Unlike a thin film, which optically behaves as a single, large phosphor particle with no optical discontinuities, a luminescent ceramic behaves as tightly packed individual phosphor particles, such that there are small optical discontinuities at the interface between different phosphor particles. Thus, luminescent ceramics are optically almost homogenous and have the same refractive index as the phosphor material forming the

luminescent ceramic. Unlike a conformal phosphor layer or a phosphor layer disposed in a transparent material such as a resin, a luminescent ceramic generally requires no binder material (such as an organic resin or epoxy) other than the phosphor itself, such that there is very little space or material of a different refractive index between the individual phosphor particles. As a result, a luminescent ceramic is transparent or translucent, unlike a conformal phosphor layer.

Luminescent ceramic layers may be attached to light emitting devices by, for example, wafer bonding, sintering, gluing with thin layers of known organic adhesives such as epoxy or silicone, gluing with high index inorganic adhesives, and gluing with sol-gel glasses.

Examples of high index adhesives include high index optical glasses such Schott glass SF59, Schott glass LaSF 3, Schott glass LaSF N18, and mixtures thereof. These glasses are available from Schott Glass Technologies Incorporated, of Duryea, Pa. Examples of other high index adhesives include high index chalcogenide glass, such as (Ge,Sb,Ga)(S,Se) chalcogenide glasses, III-V semiconductors including but not limited to GaP, InGaP, GaAs, and GaN, II-VI semiconductors including but not limited to ZnS, ZnSe, ZnTe, CdS, CdSe, and CdTe, group IV semiconductors and compounds including but not limited to Si and Ge, organic semiconductors, metal oxides including but not limited to tungsten oxide, titanium oxide, nickel oxide, zirconium oxide, indium tin oxide, and chromium oxide, metal fluorides including but not limited to magnesium fluoride and calcium fluoride, metals including but not limited to Zn, In, Mg, and Sn, yttrium aluminum garnet (YAG), phosphide compounds, arsenide compounds, antimonide compounds, nitride compounds, high index organic compounds, and mixtures or alloys thereof. Gluing with high index inorganic adhesives is described in more detail in Application Serial Numbers 09/660,317, filed September 12, 2000, and 09/880,204, filed June 12, 2001, both of which are incorporated herein by reference.

Gluing with sol-gel glasses is described in more detail in U.S. Patent 6,642,618, which is incorporated herein by reference. In embodiments where the luminescent ceramic is attached to the device by a sol-gel glass, one or more materials such as oxides of titanium, cerium, lead, gallium, bismuth, cadmium, zinc, barium, or aluminum may be included in the SiO₂ sol-gel glass to increase the index of refraction

of the glass in order to more closely match the index of the glass with the indices of the luminescent ceramic and the light emitting device. For example, a $\text{Y}_3\text{Al}_5\text{O}_{12}:\text{Ce}^{3+}$ ceramic layer may have an index of refraction of between about 1.75 and 1.8, and may be attached to a sapphire growth substrate of a semiconductor light emitting device, which sapphire substrate has an index of refraction of about 1.8. It is desirable to match the refractive index of the adhesive to the refractive indices of the $\text{Y}_3\text{Al}_5\text{O}_{12}:\text{Ce}^{3+}$ ceramic layer and the sapphire growth substrate.

In some embodiments, a luminescent ceramic serves as a growth substrate for the semiconductor light emitting device. This is especially plausible with III-nitride light emitting layers such as InGaN, which are able to be grown on a lattice-mismatched substrate (e.g., sapphire or SiC), resulting in high dislocation densities, but still exhibit high external quantum efficiency in LEDs. Thus, a semiconductor light emitting device may be grown on a luminescent ceramic in a similar manner. For example, using metal-organic chemical vapor-phase epitaxy or another epitaxial technique, a III-nitride nucleation layer is deposited, typically at low temperature (~550°C), directly on the luminescent ceramic substrate. Then, a thicker layer of GaN ('buffer' layer) is deposited, typically at higher temperature, on the III-nitride nucleation layer and coalesced into a single crystal film. Increasing the thickness of the buffer layer can reduce the total dislocation density and improve the layer quality. Finally, n-type and p-type layers are deposited, between which light emitting III-nitride active layers are included. The ability to withstand the III-nitride growth environment (e.g., temperatures greater than 1,000 °C and an NH_3 environment) will govern the choice of luminescent ceramic as a growth substrate. Because the ceramics are poly-crystalline, and the resulting III-nitride layers should be single crystal, special additional growth considerations may apply. For example, for the situation described above, it may be necessary to insert multiple low-temperature interlayers within the GaN buffer layer to 'reset' the GaN growth and avoid ceramic grain orientation effects from propagating into the III-nitride device layers. These and other techniques are known in the art for growing on lattice-mismatched substrates. Suitable growth techniques are described in, for example, U.S. Patent 6,630,692 to Goetz et al., which is assigned to the assignee of the present application and incorporated herein by reference.

Though the examples below refer to III-nitride light emitting diodes, it is

to be understood that embodiments of the invention may extend to other light emitting devices, including devices of other materials systems such as III-phosphide and III-arsenide, and other structures such as resonant cavity LEDs, laser diodes, and vertical cavity surface emitting lasers.

5 Figs. 2 and 3 illustrate III-nitride devices including luminescent ceramic layers. In the device of Fig. 2, an n-type region 42 is grown over a suitable growth substrate 40, followed by active region 43 and p-type region 44. Growth substrate 40 may be, for example, sapphire, SiC, GaN, or any other suitable growth substrate. Each of n-type region 42, active region 43, and p-type region 44 may include multiple layers
10 of different composition, thickness, and dopant concentration. For example, n-type region 42 and p-type region 44 may include contact layers optimized for ohmic contact and cladding layers optimized to contain carriers within active region 43. Active region 43 may include a single light emitting layer, or may include multiple quantum well light emitting layers separated by barrier layers.

15 In the device illustrated in Fig. 2, a portion of p-type region 44 and active region 43 are etched away to reveal a portion of n-type region 42. A p-contact 45 is formed on the remaining portion of p-type region 44 and an n-contact 46 is formed on the exposed portion of n-type region 42. In the embodiment illustrated in Fig. 2, contacts 45 and 46 are reflective such that light is extracted from the device through the back
20 side of substrate 40. Alternatively, contacts 45 and 46 may be transparent or formed in such a way that a large portion of the surfaces of p-type region 44 and n-type region 42 are left uncovered by contacts. In such devices, light may be extracted from the device through the top surface of the epitaxial structure, the surface on which contacts 45 and 46 are formed.

25 In the device illustrated in Fig. 3, the epitaxial layers are bonded to a host substrate 49 through p-contact 45. Additional layers to facilitate bonding (not shown) may be included between p-type region 44 and host 49. After the epitaxial layers are bonded to host 49, the growth substrate may be removed to expose a surface of n-type region 42. Contact to the p-side of the active region is provided through host
30 49. An n-contact 46 is formed on a portion of the exposed surface of n-type region 42. Light is extracted from the device through the top surface of n-type region 42. Growth substrate removal is described in more detail in Application Serial No. 10/804,810,

filed March 19, 2004, titled "Photonic Crystal Light Emitting Device," assigned to the assignee of the present invention, and incorporated herein by reference.

In the devices illustrated in Figs. 2 and 3, a luminescent ceramic layer 50 such as the ceramic layers described above, is attached to the surface of the device from which light is extracted; the back of substrate 40 in Fig. 2 and the top of n-type region 42 in Fig. 3. Ceramic layer 50 may be formed on or attached to any surface from which light is extracted from the device. For example, ceramic layer 50 may extend over the sides of the device illustrated in Fig. 2. Fig. 3 illustrates an optional filter 30, which allows light from active region 43 to pass into ceramic layer 50, but reflects light emitted by ceramic layer 50, such that light emitted by ceramic layer 50 is inhibited from entering device 52, where it is likely to be absorbed and lost. Examples of suitable filters include dichroic filters available from Unaxis Balzers Ltd. of Liechtenstein or Optical Coating Laboratory, Inc. of Santa Rosa, California.

Luminescent ceramic layer 50 may include a single phosphor or multiple phosphors mixed together. In some embodiments, the amount of activating dopant in the ceramic layer is graded. Fig. 4 illustrates an example of a graded doping profile in a luminescent ceramic layer. The dashed line in Fig. 4 represents the surface of the device. The phosphor in the portion of the ceramic layer closest to the device surface has the highest dopant concentration. As the distance from the device surface increases, the dopant concentration in the phosphor decreases. Though a linear dopant profile with a region of constant dopant concentration is shown in Fig. 4, it is to be understood that the grading profile may take any shape including, for example, a step-graded profile or a power law profile, and may include multiple or no regions of constant dopant concentration. In addition, in some embodiments it may be advantageous to reverse the grading profile, such that the region closest to the device surface has a small dopant concentration that increases as the distance from the device surface increases. In some embodiments, the portion of the ceramic layer furthest from the device surface may not contain any phosphor or any dopant, and may be shaped (as shown below) for light extraction.

In some embodiments, devices include multiple ceramic layers, as in the device illustrated in Fig. 5. Ceramic layer 50a is attached to device 52, which may be, for example, either of the devices illustrated in Figs. 2 and 3. Ceramic layer 50b is

attached to ceramic layer 50a. In some embodiments, one of the two ceramic layers 50a and 50b contains all the wavelength converting materials used in the device, and the other of the two ceramic layers is transparent and used as a spacer layer, if it is the ceramic layer adjacent to device 52, or as a light extraction layer, if it is the ceramic layer furthest from device 52. In some embodiments, each of ceramic layers 50a and 50b may contain a different phosphor or phosphors. Though two ceramic layers are illustrated in Fig. 5, it is to be understood that devices including more than two ceramic layers and/or more than two phosphors are within the scope of the invention. The arrangement of the different phosphors in ceramic layers 50a and 50b or ceramic layers 50a and 50b themselves may be chosen to control the interaction between the multiple phosphors in a device, as described in Application Serial No. 10/785,616 filed February 23, 2004 and incorporated herein by reference. Though ceramic layers 50a and 50b are shown stacked over device 52 in Fig. 5, other arrangements are possible and within the scope of the invention. In some embodiments, a device including one or more ceramic layers may be combined with other wavelength converting layers, such as the wavelength converting material shown in Fig. 1, or the thin films, conformal layers, and luminescent substrates described in the background section. Transparent ceramic layers that are not luminescent may be, for example, the same host material as the luminescent ceramic layer, without the activating dopant.

20 An advantage of luminescent ceramic layers is the ability to mold, grind, machine, hot stamp or polish the ceramic layers into shapes that are desirable, for example, for increased light extraction. Luminescent ceramic layers generally have high refractive indices, for example 1.75 to 1.8 for a $\text{Y}_3\text{Al}_5\text{O}_{12}:\text{Ce}^{3+}$ ceramic layer. In order to avoid total internal reflection at the interface between the high index ceramic layer and low index air, the ceramic layer may be shaped as illustrated in Figs. 6 and 7. In the device illustrated in Fig. 6, the luminescent ceramic layer 54 is shaped into a lens such as a dome lens. Light extraction from the device may be further improved by texturing the top of the ceramic layer, either randomly or in, for example, a Fresnel lens shape, as illustrated in Fig. 7. In some embodiments the top of the ceramic layer may be textured with a photonic crystal structure, such as a periodic lattice of holes formed in the ceramic. The shaped ceramic layer may be smaller than or the same size as face of device 52 to which it is attached or it may be larger than the face of device 52 to

which it is attached, as illustrated in Figs. 6 and 7. In devices such as Fig. 7, favorable light extraction is expected for shaped ceramic layers having a bottom length at least twice the length of the face of device 52 on which the ceramic layer is mounted. In some embodiments, the wavelength converting material is confined to the portion of the ceramic layer closest to the device 52. In other embodiments, as illustrated in Fig. 7, the wavelength converting material is provided in a first ceramic layer 50a, then attached to a second, shaped, transparent ceramic layer 50b.

In some embodiments, the surface of the top ceramic layer is roughened to increase scattering necessary to mix the light, for example, in a device where light from the light emitting device and one or more wavelength converting layers mixes to form white light. In other embodiments, sufficient mixing may be accomplished by secondary optics such as a lens or light guide, as is known in the art.

A further advantage of luminescent ceramic layers is the favorable thermal properties of ceramics. A device including a luminescent ceramic layer and a heat extraction structure is illustrated in Fig. 8. As in Fig. 7, Fig. 8 includes a transparent or luminescent ceramic layer 50b that is shaped for light extraction. An optional additional transparent or luminescent ceramic layer 50a is disposed between layer 50b and device 52. Device 52 is mounted on a submount 58, for example as a flip chip as illustrated in Fig. 2. Submount 58 and host substrate 49 of Fig. 3, may be, for example, metals such as Cu foil, Mo, Cu/Mo, and Cu/W; semiconductors with metal contacts, such as Si with ohmic contacts and GaAs with ohmic contacts including, for example, one or more of Pd, Ge, Ti, Au, Ni, Ag; and ceramics such as compressed diamond. Layers 56 are thermally conductive materials that connect ceramic layer 50b to submount 58, potentially reducing the temperature of luminescent ceramic layer 50a and/or 50b, and thereby increasing light output. Material suitable for layers 56 include the submount material described above. The arrangement illustrated in Fig. 8 is particularly useful to extract heat from flip chip devices with conductive substrates, such as SiC.

An example of a cerium-doped yttrium aluminum garnet ceramic slab diffusion-bonded to a sapphire substrate is given below.

Diffusion-bonded YAG-sapphire composites are advantageous because of their high mechanical strength and excellent optical quality. According to the phase

diagram yttria–alumina within the composition range Al_2O_3 and $3 \text{ Y}_2\text{O}_3 \cdot 5 \text{ Al}_2\text{O}_3$, no other phase exists except an eutecticum with 33% Al. Therefore, a sinterbonded YAG-sapphire composite has an average refractive index at the (eutectoidic) interface between YAG ceramic ($n_i = 1.84$) and sapphire substrate ($n_i = 1.76$) and thus a high quality optical contact can be obtained. In addition, because of the similar expansion coefficients of YAG and sapphire (YAG: $6.9 \times 10^{-6} \text{ K}^{-1}$, Al_2O_3 : $8.6 \times 10^{-6} \text{ K}^{-1}$), sinterbonded wafers with low mechanical stress can be produced.

A diffusion-bonded YAG:Ce ceramic–sapphire wafer may be formed as follows:

10 a) Production of YAG:Ce ceramic: 40 g Y_2O_3 (99.998%), 32 g Al_2O_3 (99.999%), and 3.44 g CeO_2 are milled with 1.5 kg high purity alumina balls (2 mm diameter) in isopropanol on a roller bench for 12 hrs. The dried precursor powder is then calcined at 1300°C for two hours under CO atmosphere. The YAG powder obtained is then deagglomerated with a planet ball mill (agate balls) under ethanol. The ceramic slurry is then slip casted to obtain a ceramic green body after drying. The green bodies are then sintered between graphite plates at 1700°C for two hours.

20 b) Diffusion-bonding of a sapphire waver and a YAG:Ce ceramic: The ground and polished sapphire and YAG wafers are diffusion bonded in a uniaxial hot pressing apparatus (HUP). For this purpose sapphire and YAG wafers are stacked between tungsten foils (0.5 mm thickness) and placed in a graphite pressing die. To increase the speed of processing several sapphire / YAG:Ce ceramic / tungsten foil stacks can be stacked and processed simultaneously.

25 After evacuation of the HUP apparatus the temperature is first increased to 1700°C within 4 hrs without applying external pressure. Then a uniaxial pressure of 300 bar is applied and kept constant for 2 hrs. After the dwell time the temperature is lowered to 1300°C within 2 hrs by keeping the pressure constant. Finally the system is cooled down to room temperature within 6 hrs after releasing the pressure.

30 c) Post processing of sinterbonded sapphire–YAG:Ce wafers: After grinding and polishing of the surfaces of the sinterbonded wafers, the samples are annealed for 2 hrs at 1300°C in air (heating rate: 300 K/hr), then cooled down to room temperature within 12 hrs.

Having described the invention in detail, those skilled in the art will

appreciate that, given the present disclosure, modifications may be made to the invention without departing from the spirit of the inventive concept described herein. Therefore, it is not intended that the scope of the invention be limited to the specific embodiments illustrated and described.

CLAIMS:

What is being claimed is:

1. A structure comprising: a semiconductor light emitting device comprising a light emitting layer disposed between an n-type region and a p-type region; and a first ceramic layer disposed in a path of light emitted by the light emitting layer, the first ceramic layer comprising a wavelength converting material.
2. The structure of claim 1 wherein the first ceramic layer comprises a rigid agglomerate of phosphor particles.
3. The structure of claim 2 wherein the first ceramic layer is substantially free of binder material.
4. The structure of claim 2 wherein the first ceramic layer is formed by heating powder phosphor particles at a pressure greater than atmospheric pressure until a surface of the phosphor particles begins to soften and the phosphor particles stick together.
5. The structure of claim 1 further comprising an adhesive layer disposed between the first ceramic layer and the semiconductor light emitting device.
6. The structure of claim 5 wherein the adhesive layer is one of an organic adhesive, epoxy, silicone, an inorganic adhesive, and a sol-gel glass.
7. The structure of claim 5 wherein the adhesive layer comprises one of Schott glass SF59, Schott glass LaSF 3, Schott glass LaSF N18, chalcogenide glass, (Ge,Sb,Ga)(S,Se) chalcogenide glass, a III-V semiconductor, GaP, InGaP, GaAs, GaN,

a II-VI semiconductor, ZnS, ZnSe, ZnTe, CdS, CdSe, CdTe, a group IV semiconductor, Si, Ge, an organic semiconductor, a metal oxide, tungsten oxide, titanium oxide, nickel oxide, zirconium oxide, indium tin oxide, and chromium oxide, a metal fluoride, magnesium fluoride, calcium fluoride, a metal, Zn, In, Mg, and Sn, yttrium aluminum
5 garnet, a phosphide compound, an arsenide compound, an antimonide compound, a nitride compound, and a high index organic compound.

8. The structure of claim 5 wherein the first ceramic layer is substantially free of adhesive material.

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9. The structure of claim 1 further comprising an interface between the semiconductor light emitting device and the first ceramic layer, the interface formed by pressing the semiconductor light emitting device and the first ceramic layer together at a temperature greater than room temperature and a pressure greater than atmospheric
15 pressure.

10. The structure of claim 1 wherein a surface of the first ceramic layer comprises a lens.

20 11. The structure of claim 1 wherein a surface of the first ceramic layer comprises a dome.

12. The structure of claim 1 wherein a surface of the first ceramic layer comprises a Fresnel lens.

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13. The structure of claim 1 wherein a surface of the first ceramic layer comprises a photonic crystal structure.

14. The structure of claim 1 wherein a surface of the first ceramic layer is
30 textured.

15. The structure of claim 1 wherein the first ceramic layer comprises a phosphor doped with a dopant, wherein a concentration of dopant is graded.

16. The structure of claim 15 wherein: the concentration is graded from a first concentration in a first portion of the first ceramic layer to a second concentration in a second portion of the first ceramic layer; the first concentration is greater than the second concentration; and the first portion is closer to the semiconductor light emitting device than the second portion.

17. The structure of claim 1 wherein the semiconductor light emitting device comprises a growth substrate, and the first ceramic layer is attached to the growth substrate.

18. The structure of claim 1 wherein the semiconductor light emitting device comprises a host substrate, and the first ceramic layer is attached to a surface of the semiconductor light emitting device opposite the host substrate.

19. The structure of claim 1 further comprising a second ceramic layer disposed in a path of light emitted by the light emitting layer.

20. The structure of claim 19 wherein: the first ceramic layer comprises a first wavelength converting material; and the second ceramic layer comprises a second wavelength converting material.

21. The structure of claim 19 wherein: the first ceramic layer comprises a wavelength converting material; and the second ceramic layer does not include a wavelength converting material.

22. The structure of claim 19 wherein a surface area of a surface of one of the first and second ceramic layers is greater than a surface area of a surface of the semiconductor light emitting device from which light is extracted from the device.

23. The structure of claim 1 wherein the wavelength converting material comprises one of $(\text{Lu}_{1-x-y-a-b}\text{Y}_x\text{Gd}_y)_3(\text{Al}_{1-z}\text{Ga}_z)_5\text{O}_{12}:\text{Ce}_a\text{Pr}_b$ wherein $0 < x < 1$, $0 < y < 1$, $0 < z \leq 0.1$, $0 < a \leq 0.2$ and $0 < b \leq 0.1$; $\text{Lu}_3\text{Al}_5\text{O}_{12}:\text{Ce}^{3+}$; and $\text{Y}_3\text{Al}_5\text{O}_{12}:\text{Ce}^{3+}$.

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24. The structure of claim 1 further comprising a dichroic filter disposed between the first ceramic layer and the semiconductor light emitting device.

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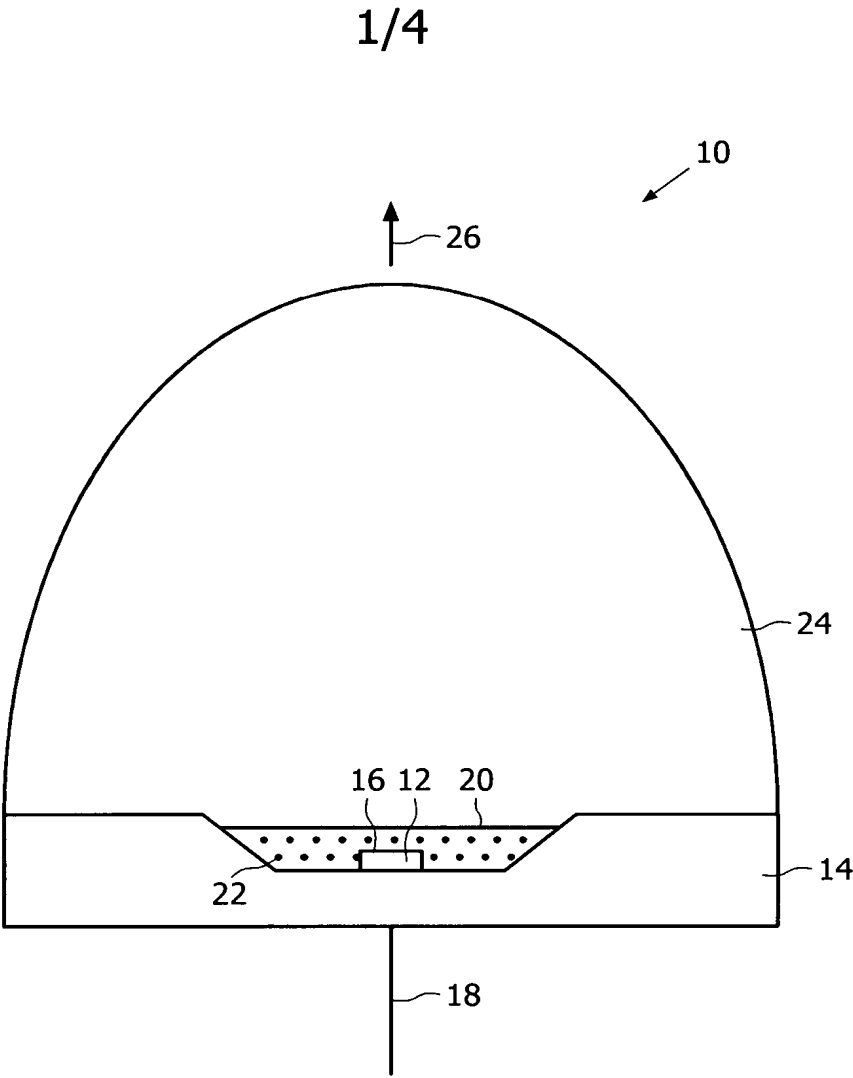


FIG. 1 PRIOR ART

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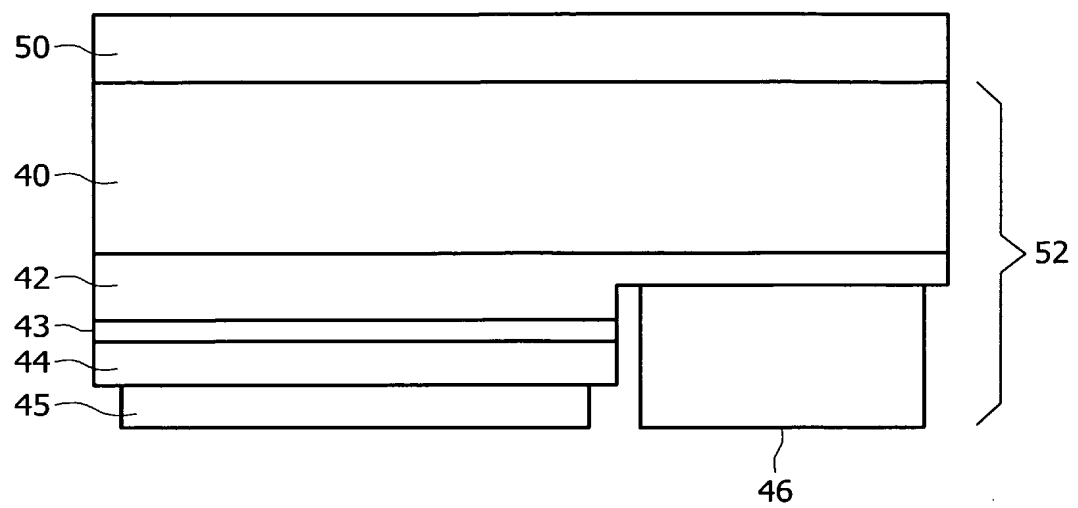


FIG. 2

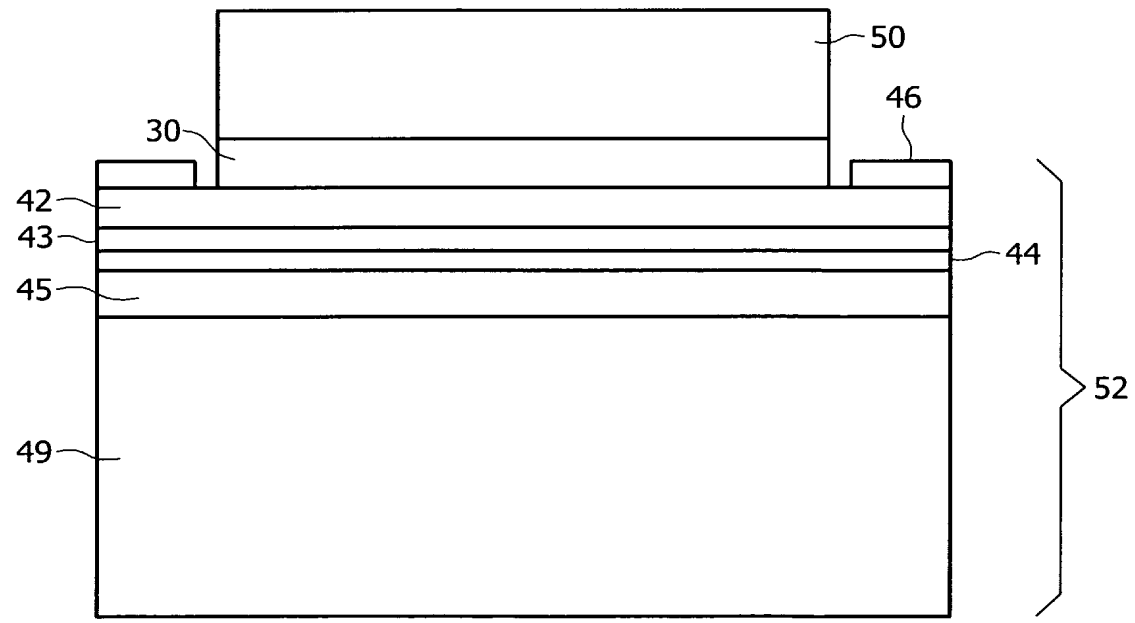


FIG. 3

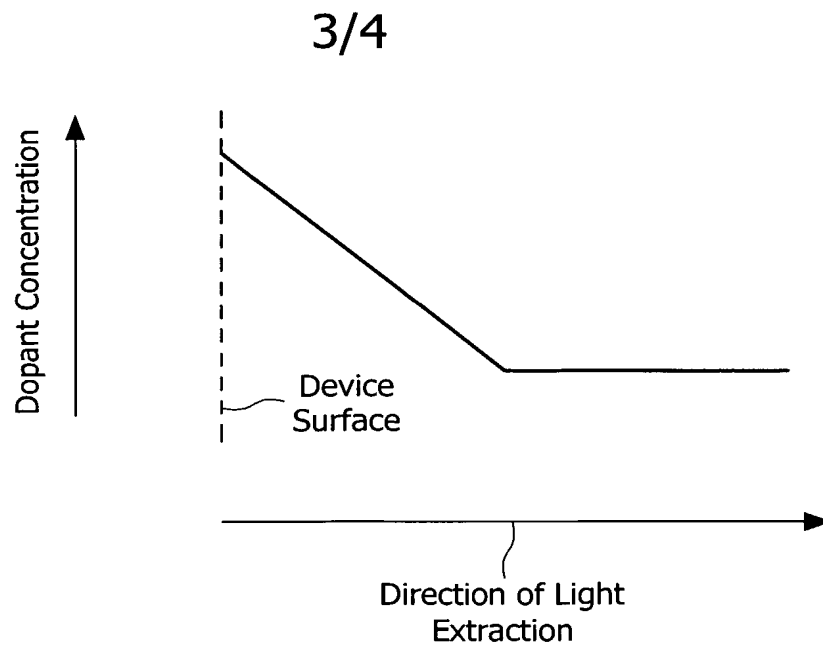


FIG. 4

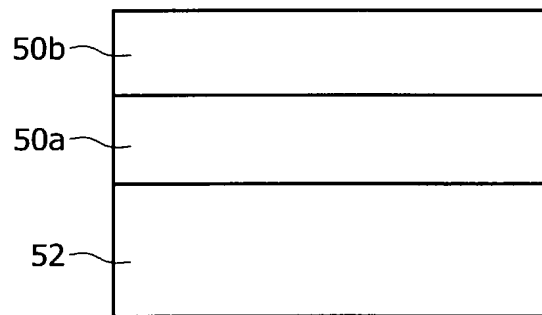


FIG. 5

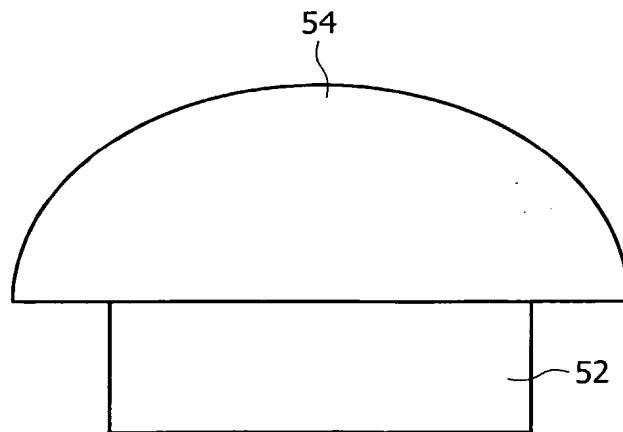


FIG. 6

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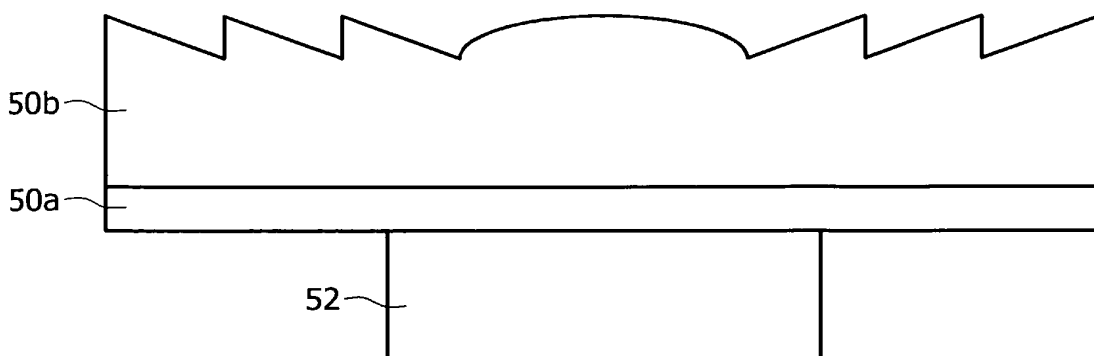


FIG. 7

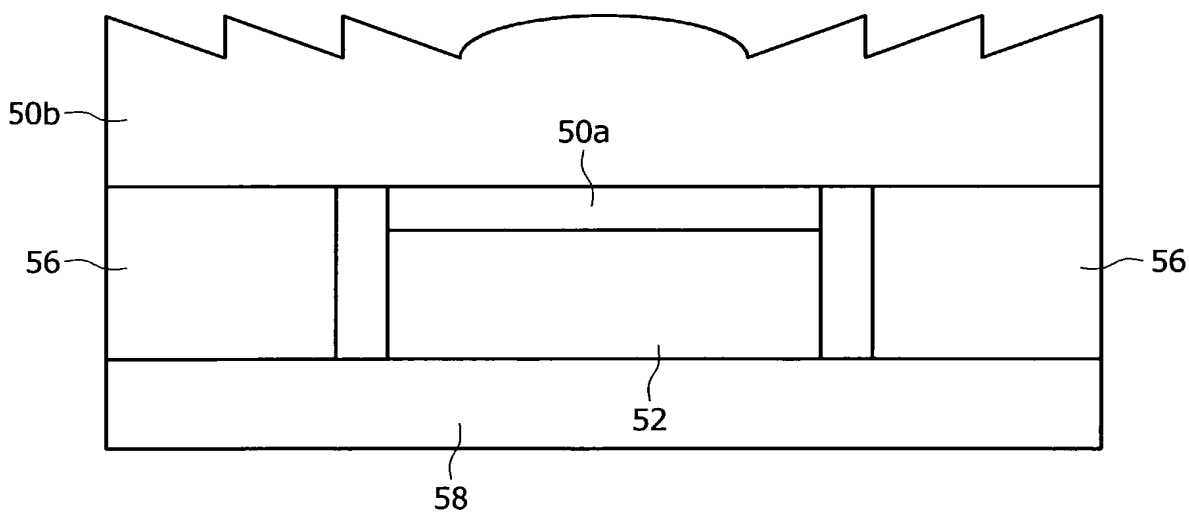


FIG. 8

INTERNATIONAL SEARCH REPORT

International Application No

PCT/IB2005/051702

A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 H01L33/00

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHEDMinimum documentation searched (classification system followed by classification symbols)
IPC 7 H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	DE 103 49 038 A1 (OSRAM OPTO SEMICONDUCTORS GMBH) 13 May 2004 (2004-05-13) paragraphs '0001! - '0003!	1-23
Y	paragraphs '0006!, '0007! paragraphs '0011! - '0027! paragraphs '0030! - '0054!	24
Y	US 3 932 881 A (MITA ET AL) 13 January 1976 (1976-01-13) column 4, line 4 - line 12	24
X	US 2003/067264 A1 (TAKEKUMA AKIRA) 10 April 2003 (2003-04-10) paragraph '0025!	1
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☒ Further documents are listed in the continuation of box C.☒ Patent family members are listed in annex.

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Date of the actual completion of the international search

21 October 2005

Date of mailing of the international search report

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Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax: (+31-70) 340-3016

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INTERNATIONAL SEARCH REPORT

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Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 6 630 691 B1 (MUELLER-MACH REGINA B ET AL) 7 October 2003 (2003-10-07) column 3, line 56 - column 4, line 36 -----	1
X	US 2003/025449 A1 (ROSSNER WOLFGANG) 6 February 2003 (2003-02-06) paragraphs '0009! - '0013! -----	1
X	DE 100 27 199 A1 (SANKEN ELECTRIC CO. LTD., NIIZA) 11 January 2001 (2001-01-11) sentence 4, paragraph 53 - sentence 5, paragraph 14 -----	1

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/IB2005/051702

Patent document cited in search report		Publication date	Patent family member(s)	Publication date
DE 10349038	A1	13-05-2004	JP 2004146835 A US 2004145308 A1	20-05-2004 29-07-2004
US 3932881	A	13-01-1976	NONE	
US 2003067264	A1	10-04-2003	JP 2003124525 A	25-04-2003
US 6630691	B1	07-10-2003	AU 7720900 A EP 1142034 A1 JP 2001203383 A WO 0124285 A1	30-04-2001 10-10-2001 27-07-2001 05-04-2001
US 2003025449	A1	06-02-2003	DE 10137641 A1 DE 20209131 U1 GB 2381125 A NL 1021201 C1	20-02-2003 17-10-2002 23-04-2003 04-02-2003
DE 10027199	A1	11-01-2001	JP 3503131 B2 JP 2000349340 A US 6756731 B1	02-03-2004 15-12-2000 29-06-2004